### EXPT NO. : DESIGN OF ADDER AND SUBTRACTOR

**DATE :**

**AIM:**

To design and construct half adder, full adder, half subtractor and full subtractor circuits and verify the truth table using logic gates.

**APPARATUS REQUIRED:**

|  |  |  |  |
| --- | --- | --- | --- |
| Sl.No. | COMPONENT | SPECIFICATION | QTY. |
| 1. | AND GATE | IC 7408 | 1 |
| 2. | X-OR GATE | IC 7486 | 1 |
| 3. | NOT GATE | IC 7404 | 1 |
| 4. | OR GATE | IC 7432 | 1 |
| 3. | IC TRAINER KIT | - | 1 |
| 4. | PATCH CORDS | - | 23 |

**THEORY:**

**HALF ADDER:**

A half adder has two inputs for the two bits to be added and two outputs one from the sum ‘ S’ and other from the carry ‘ c’ into the higher adder position. Above circuit is called as a carry signal from the addition of the less significant bits sum from the X-OR Gate the carry out from the AND gate.

**FULL ADDER:**

A full adder is a combinational circuit that forms the arithmetic sum of input; it consists of three inputs and two outputs. A full adder is useful to add three bits at a time but a half adder cannot do so. In full adder sum output will be taken from X-OR Gate, carry output will be taken from OR Gate.

**HALF SUBTRACTOR:**

The half subtractor is constructed using X-OR and AND Gate. The half subtractor has two input and two outputs. The outputs are difference and borrow. The difference can be applied using X-OR Gate, borrow output can be implemented using an AND Gate and an inverter.

**FULL SUBTRACTOR:**

The full subtractor is a combination of X-OR, AND, OR, NOT Gates. In a full subtractor the logic circuit should have three inputs and two outputs. The two half subtractor put together gives a full subtractor .The first half subtractor will be C and A B. The output will be difference output of full subtractor. The expression AB assembles the borrow output of the half subtractor and the second term is the inverted difference output of first X-OR.

**LOGIC DIAGRAM:**

**HALF ADDER**

**A diagram of a rocket

AI-generated content may be incorrect.**

**TRUTH TABLE:**

|  |  |  |  |
| --- | --- | --- | --- |
| **A** | **B** | **CARRY** | **SUM** |
| **0**  **0**  **1**  **1** | **0**  **1**  **0**  **1** | **0**  **0**  **0**  **1** | **0**  **1**  **1**  **0** |

**K-Map for SUM: K-Map for CARRY:**

**A white square with black numbers and a triangle

AI-generated content may be incorrect.A black and white diagram

AI-generated content may be incorrect.**

**SUM = A’B + AB’ CARRY = AB**

**LOGIC DIAGRAM:**

**FULL ADDER**

**FULL ADDER USING TWO HALF ADDER**

**A diagram of a rocket

AI-generated content may be incorrect.**

**TRUTH TABLE:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **CARRY** | **SUM** |
| **0**  **0**  **0**  **0**  **1**  **1**  **1**  **1** | **0**  **0**  **1**  **1**  **0**  **0**  **1**  **1** | **0**  **1**  **0**  **1**  **0**  **1**  **0**  **1** | **0**  **0**  **0**  **1**  **0**  **1**  **1**  **1** | **0**  **1**  **1**  **0**  **1**  **0**  **0**  **1** |

**K-Map for SUM:**

**A grid of squares with numbers

AI-generated content may be incorrect.**

**SUM = A’B’C + A’BC’ + ABC’ + ABC**

**K-Map for CARRY:**

**A diagram of a number

AI-generated content may be incorrect.**

**CARRY = AB + BC + AC**

#### LOGIC DIAGRAM:

**HALF SUBTRACTOR**

A diagram of a circuit

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#### TRUTH TABLE:

|  |  |  |  |
| --- | --- | --- | --- |
| **A** | **B** | **BORROW** | **DIFFERENCE** |
| **0**  **0**  **1**  **1** | **0**  **1**  **0**  **1** | **0**  **1**  **0**  **0** | **0**  **1**  **1**  **0** |

**K-Map for DIFFERENCE:**

**A white square with black numbers and a triangle

AI-generated content may be incorrect.**

**DIFFERENCE = A’B + AB’**

**K-Map for BORROW:**

**A diagram of a square with numbers and a triangle

AI-generated content may be incorrect.**

**BORROW = A’B**

**LOGIC DIAGRAM:**

**FULL SUBTRACTOR**

**A diagram of a circuit

AI-generated content may be incorrect.**

**FULL SUBTRACTOR USING TWO HALF SUBTRACTOR:**

**A diagram of a circuit

AI-generated content may be incorrect.**

**TRUTH TABLE:**

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **A** | **B** | **C** | **BORROW** | **DIFFERENCE** |
| **0**  **0**  **0**  **0**  **1**  **1**  **1**  **1** | **0**  **0**  **1**  **1**  **0**  **0**  **1**  **1** | **0**  **1**  **0**  **1**  **0**  **1**  **0**  **1** | **0**  **1**  **1**  **1**  **0**  **0**  **0**  **1** | **0**  **1**  **1**  **0**  **1**  **0**  **0**  **1** |

**K-Map for Difference:**

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AI-generated content may be incorrect.**

**Difference = A’B’C + A’BC’ + AB’C’ + ABC**

**K-Map for Borrow:**

**A diagram of a number

AI-generated content may be incorrect.**

**Borrow = A’B + BC + A’C**

**PROCEEDURE:**

1. Connections are given as per circuit diagram.
2. Logical inputs are given as per circuit diagram.
3. Observe the output and verify the truth table.

**RESULT:**